

FIG. 1 PRIOR ART

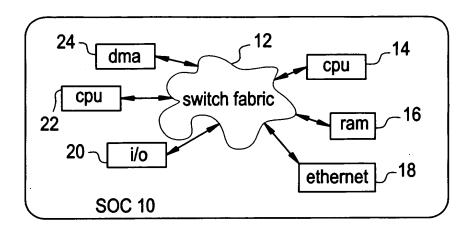


FIG. 2

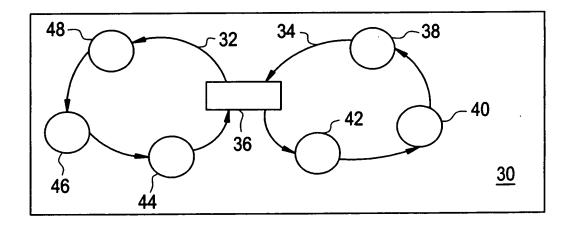




FIG. 3

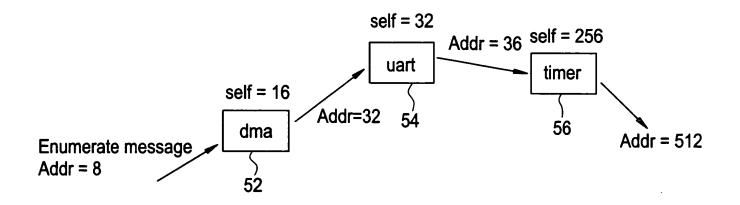


FIG. 4

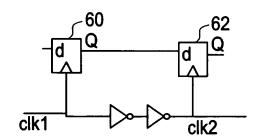


FIG. 5

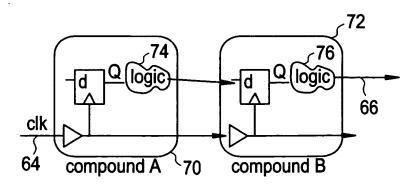






FIG. 6

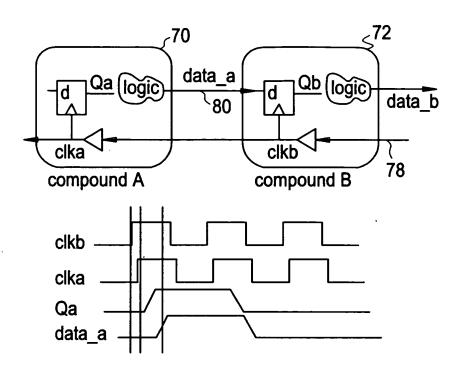


FIG. 7

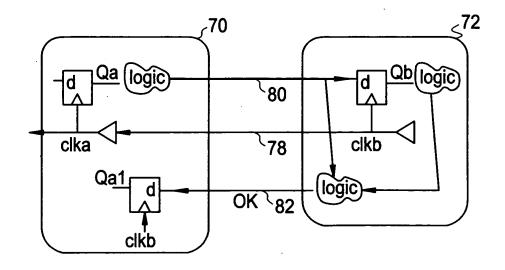




FIG. 8

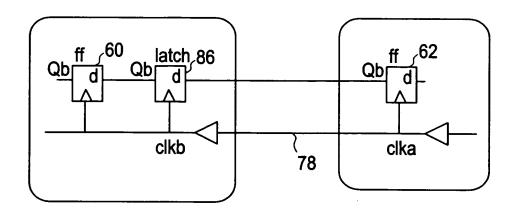
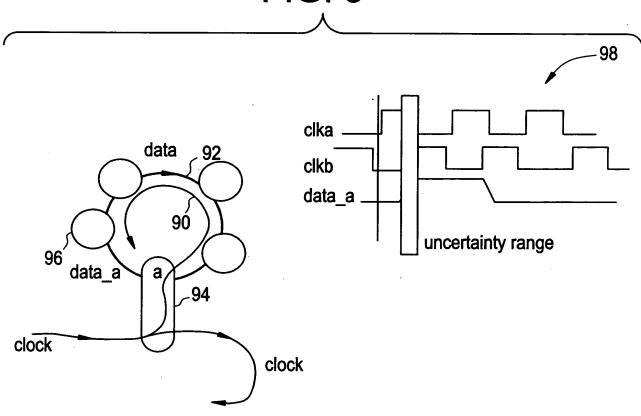
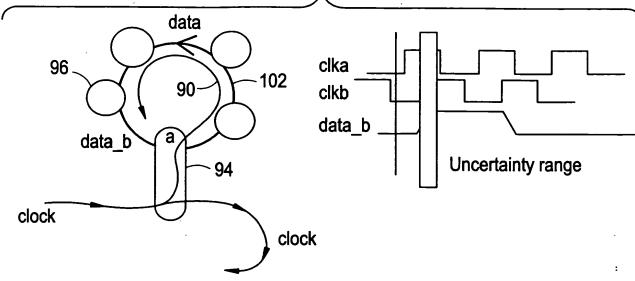


FIG. 9



HODDSHULL LOGICOL

FIG. 10



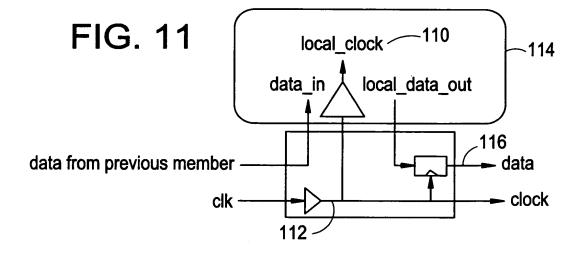


FIG. 12

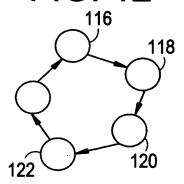


FIG. 13

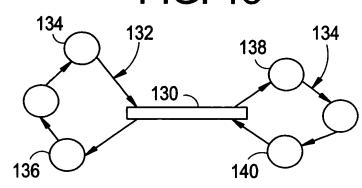




FIG. 14

150

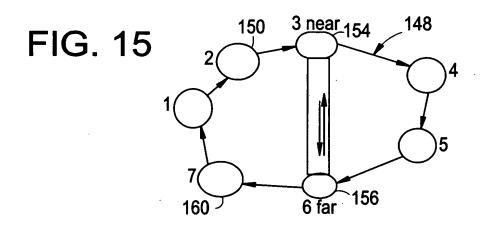
158

158

150

158

158



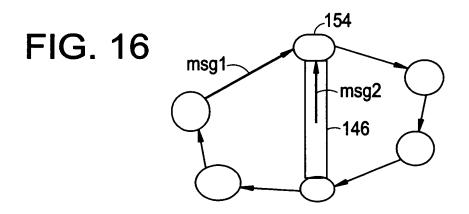




FIG. 17

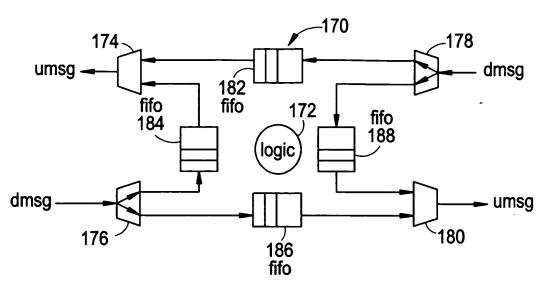
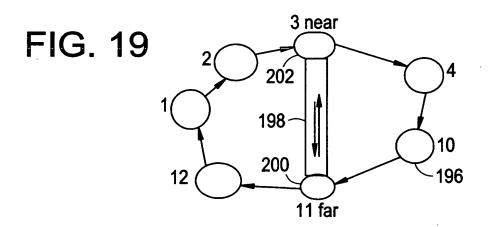


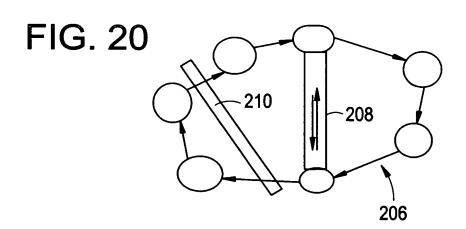
FIG. 18

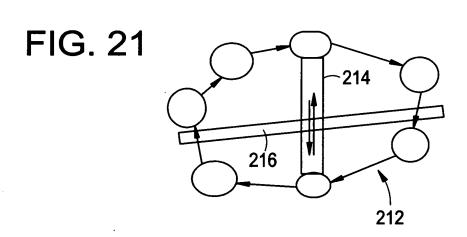
192

near far



TODELTET OPEROL





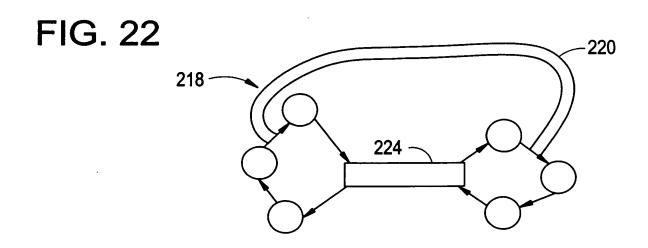


FIG. 23

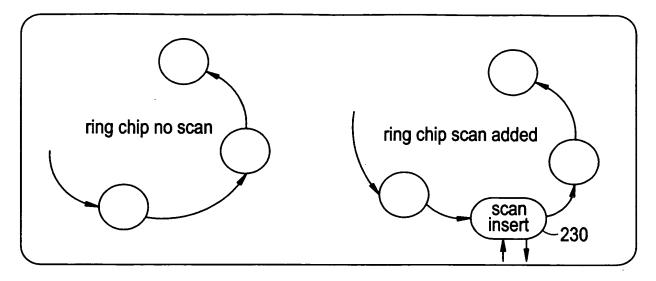


FIG. 24

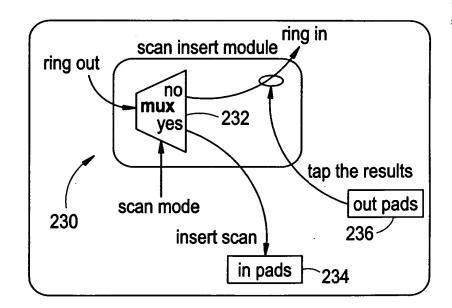


FIG. 25

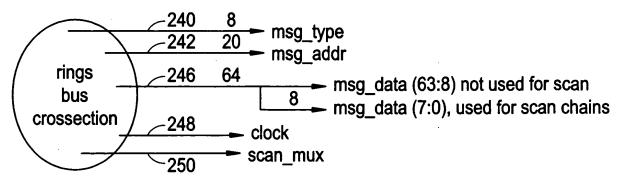




FIG. 26

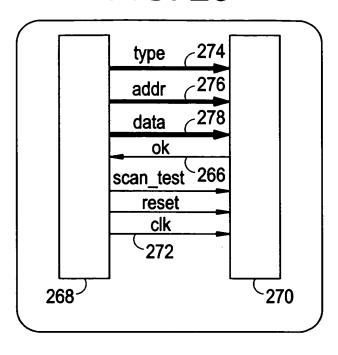


FIG. 27

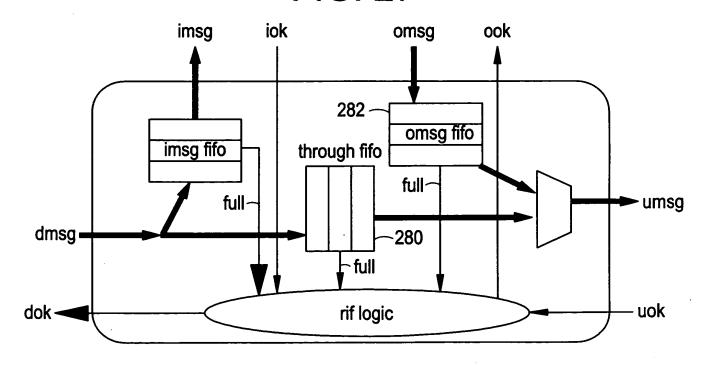




FIG. 29

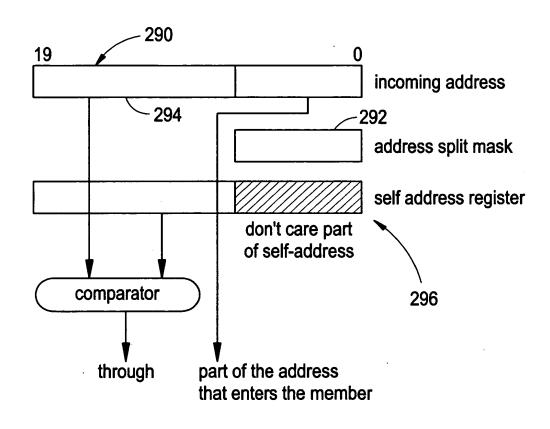




FIG. 30

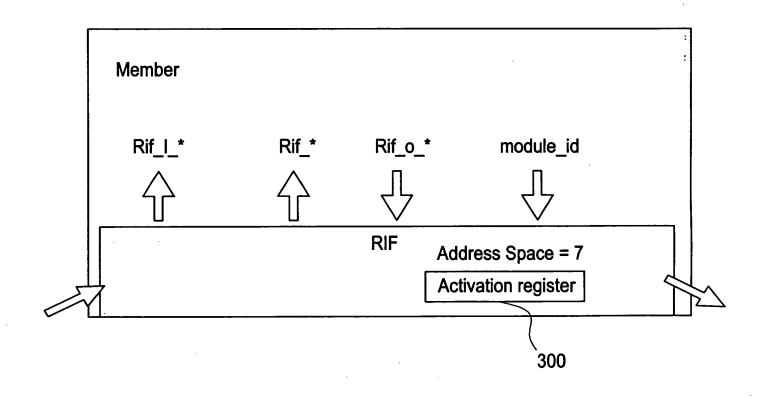




FIG. 31

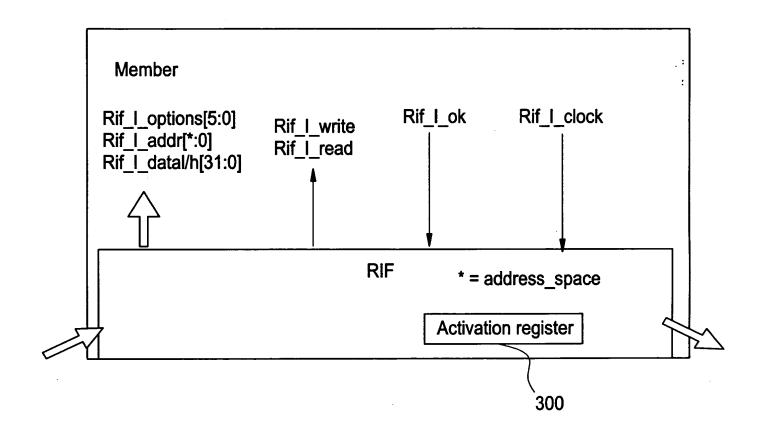




FIG. 32

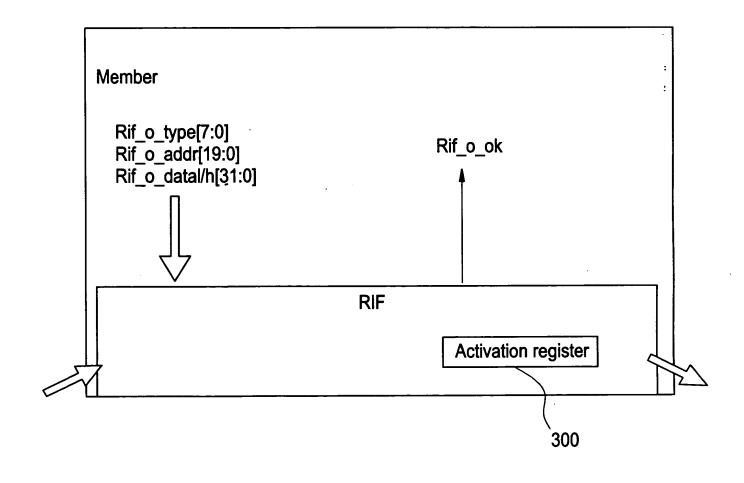
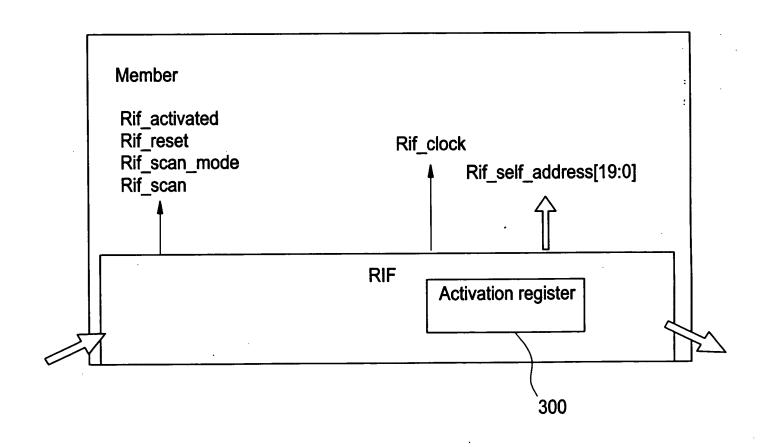




FIG. 33

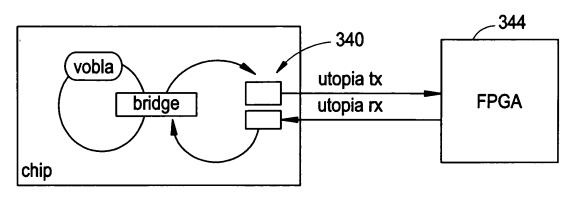


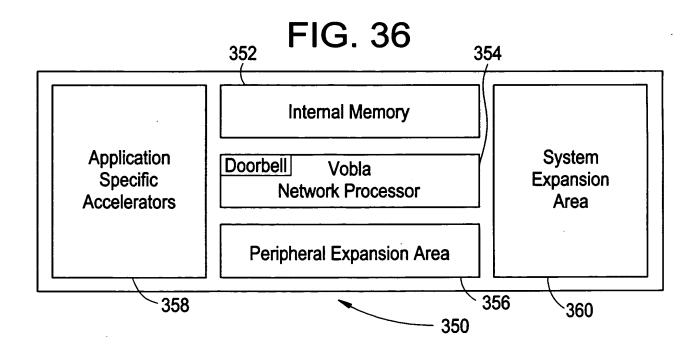
332

334

336

FIG. 35





TOOSTED TEEHOOD

MINDEX SPR

(spr index - 3)



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FIG. 41

R1 register

c - carry mb - reflection of the RAM mult-reader busy indication

FIG. 42

REFETCH SPR **NEXT REFETCH** REFETCH (spr index - 0) 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0 654321 0 9 8 7 6 5 4 3 2 1 0 DOOR UMASK ... **CTID** TASK SPR COUNT NTID 442 (spr index - 1) 19876543210 3 DPT BAAR BBA TRAP SPR (spr index - 2) 19876543210 2 0987

INDEX2

INDEX1

446



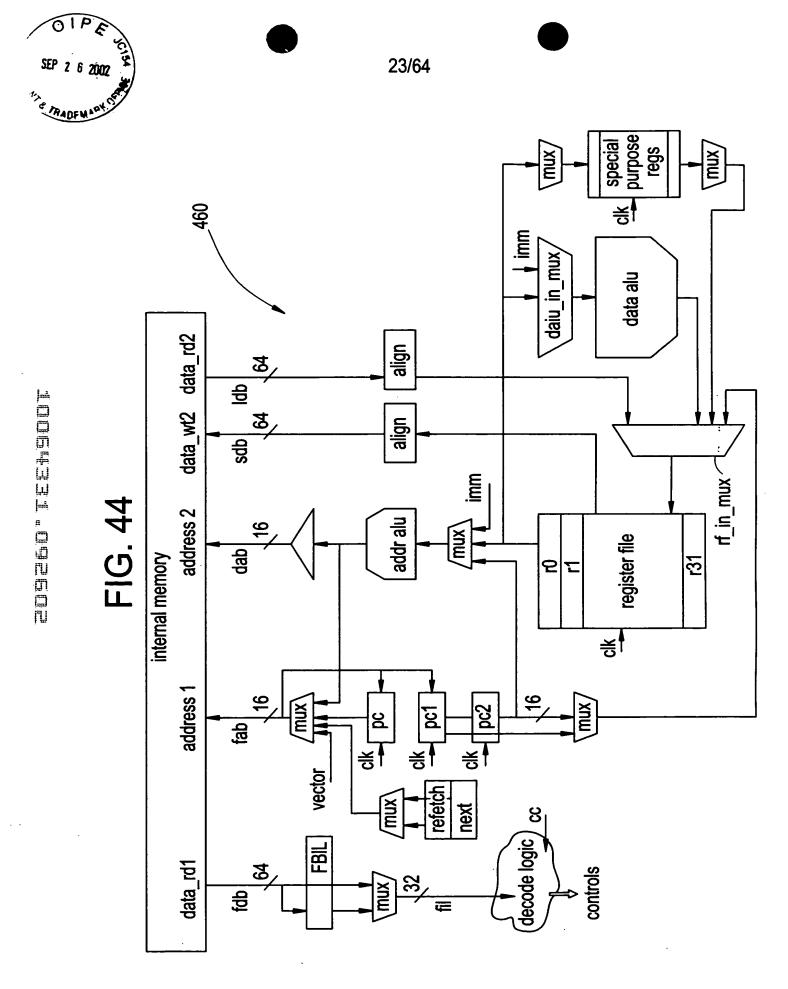
FIG. 43

	450		
	common task data		
	task		task
r27	fragment 1 data	task fragment 2 data	fragment 3 data
	data of all level2 functions		
	level1 f1 data	level1 f2 data	level1 f3 data

size of level0 frame part is different for each task type

size of level2 frame part is constant size of level1 frame part is different per each task

type



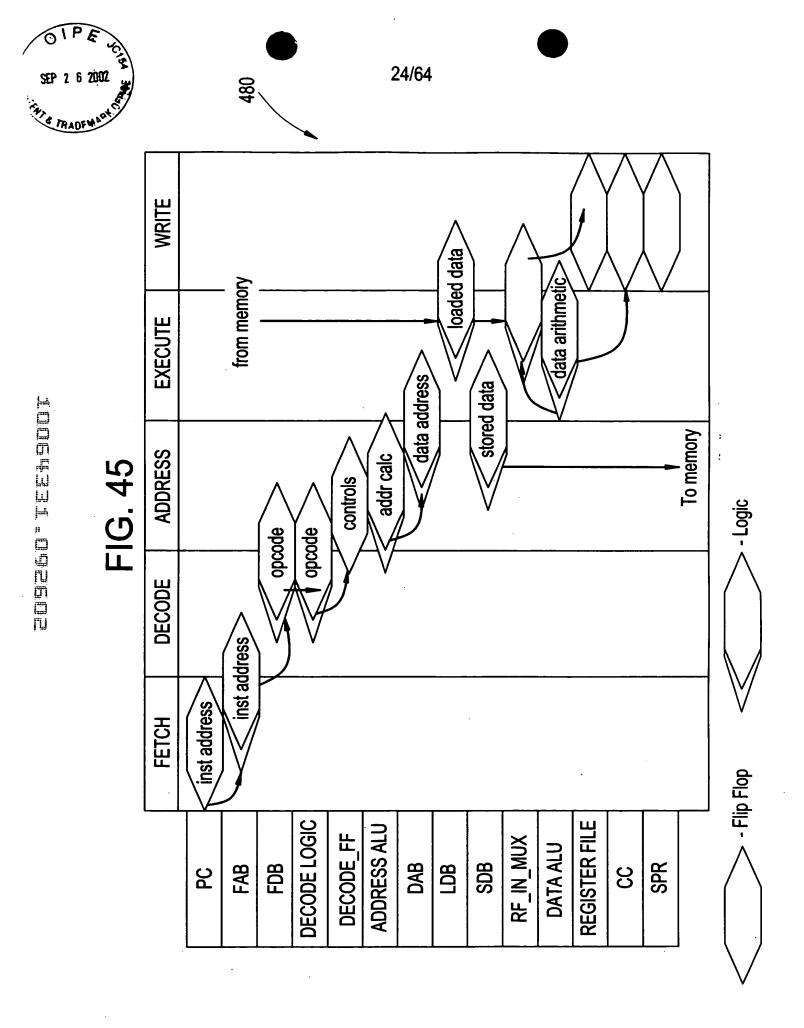
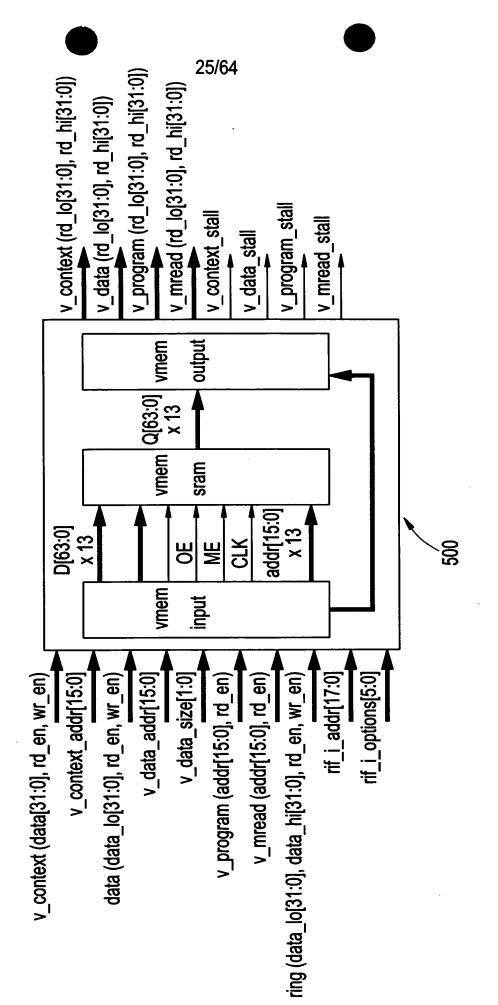


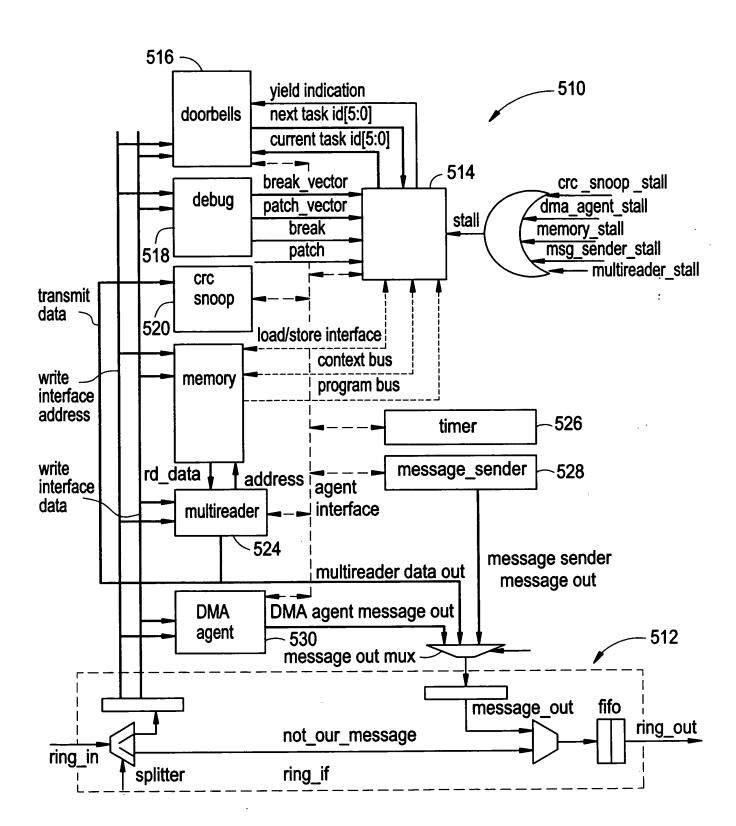


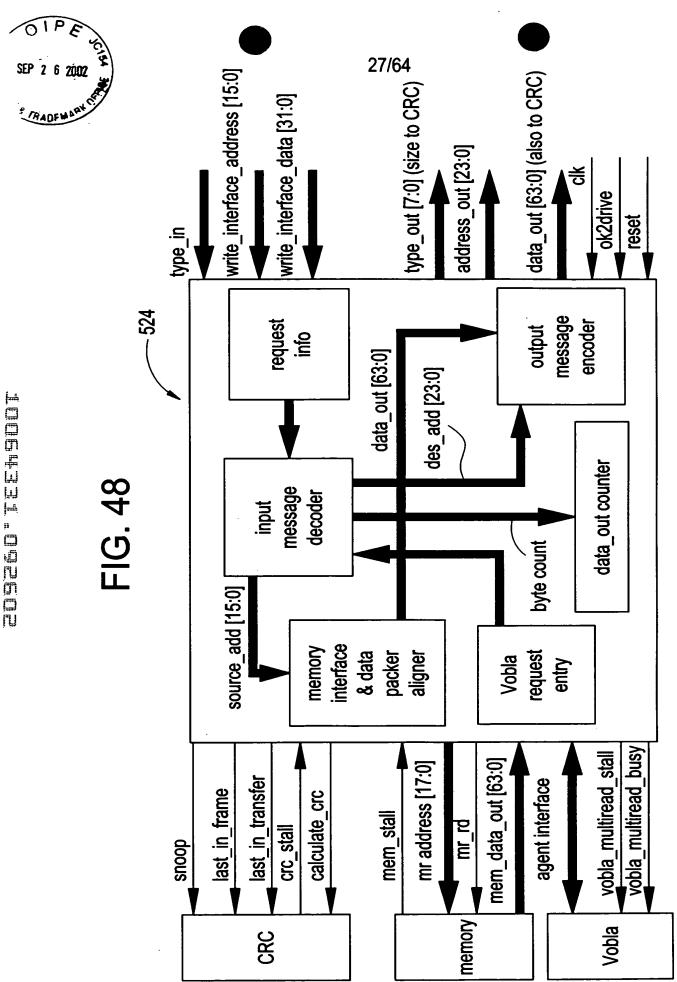
FIG. 46



TOOKYJALOJESOL

FIG. 47





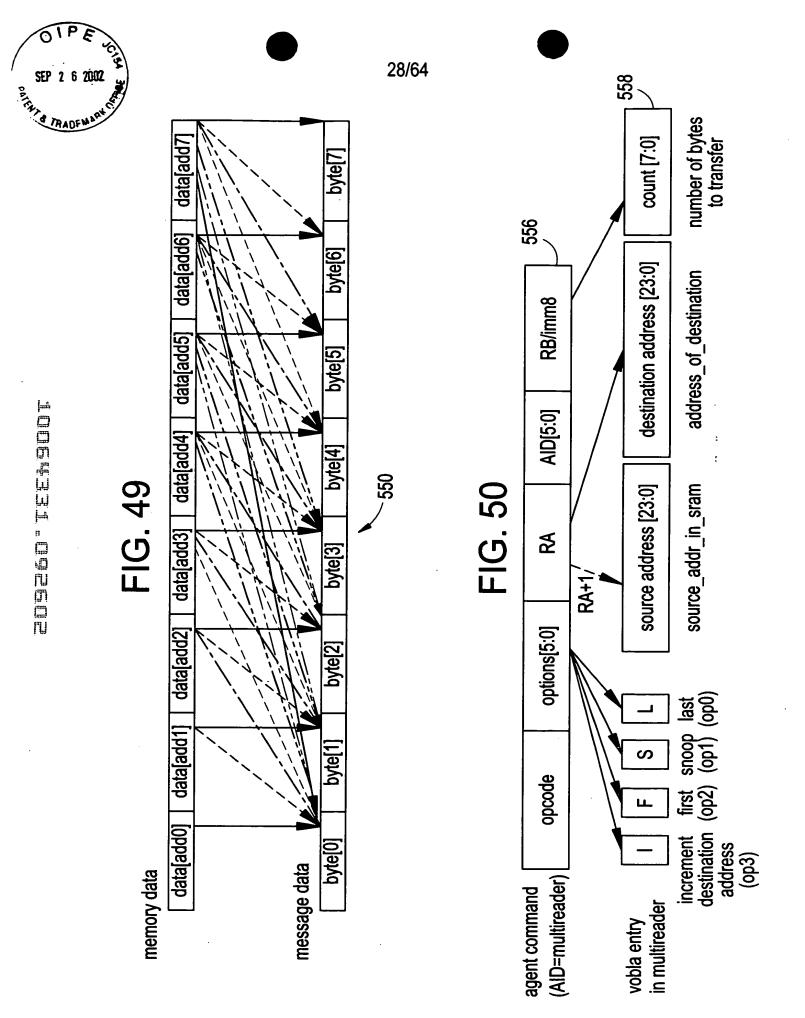
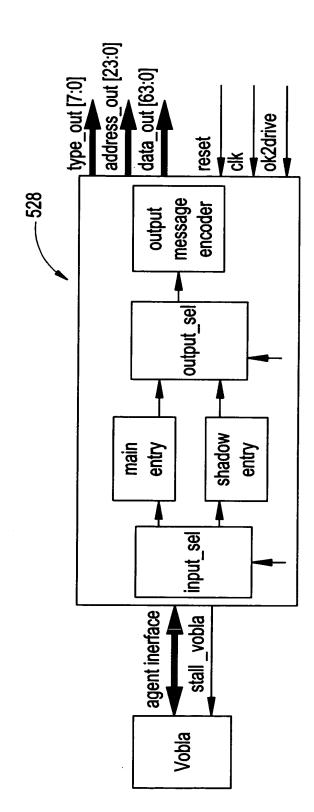




FIG. 51







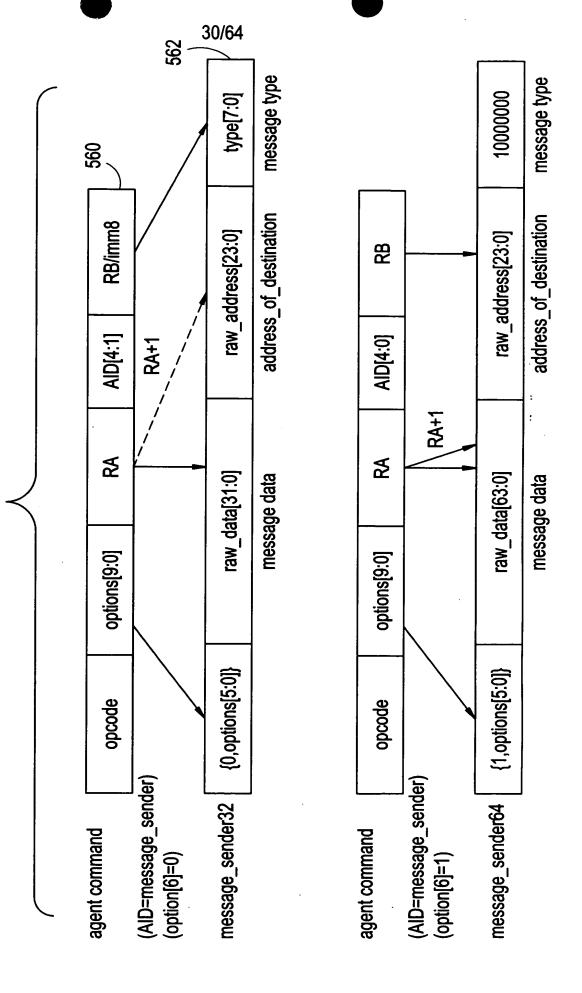




FIG. 53

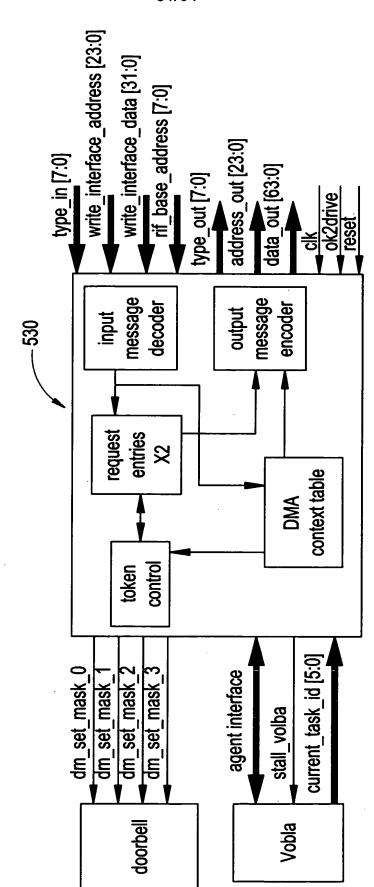




FIG. 54

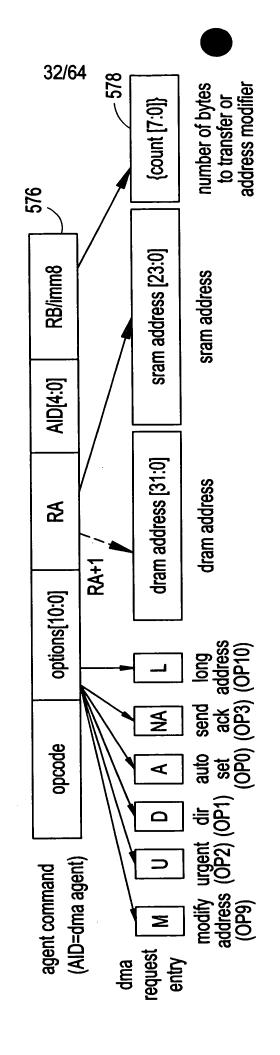




FIG. 55

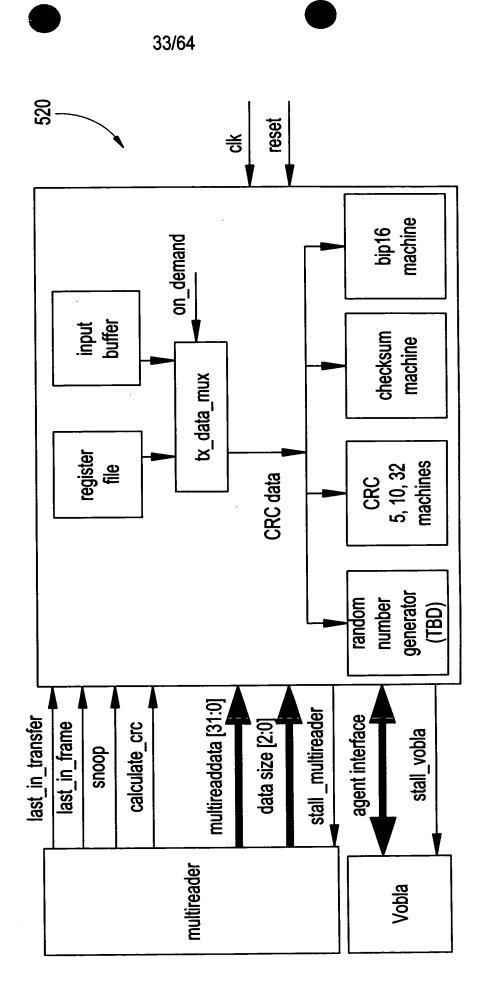




FIG. 56

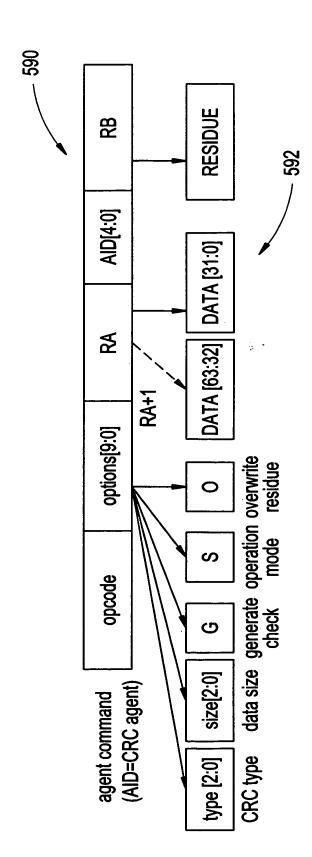




FIG. 57

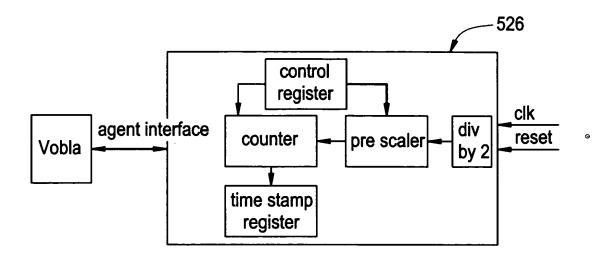
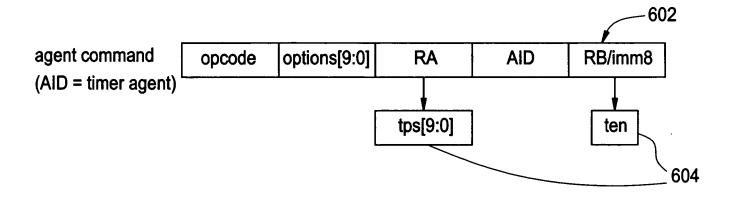
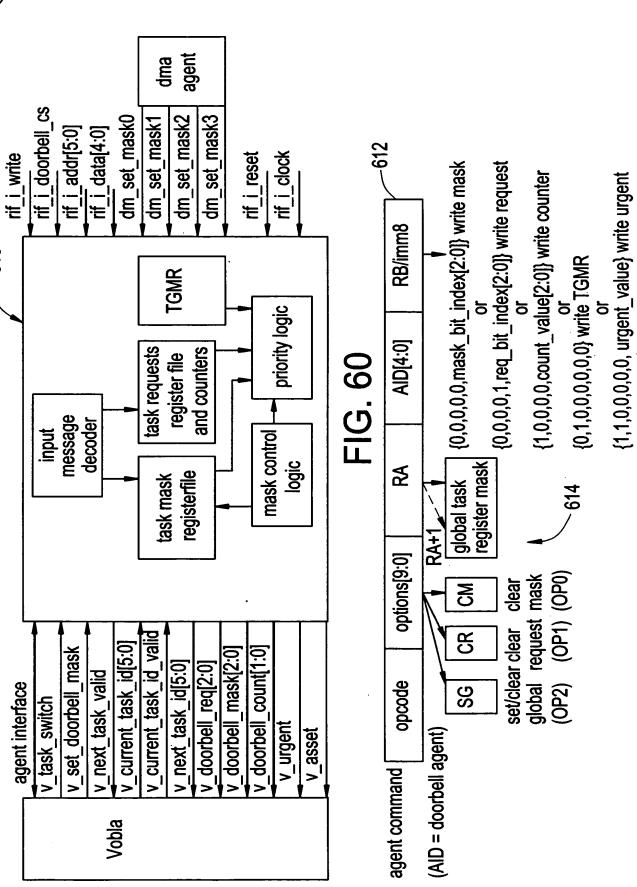


FIG. 58





EBB

RMI10b)

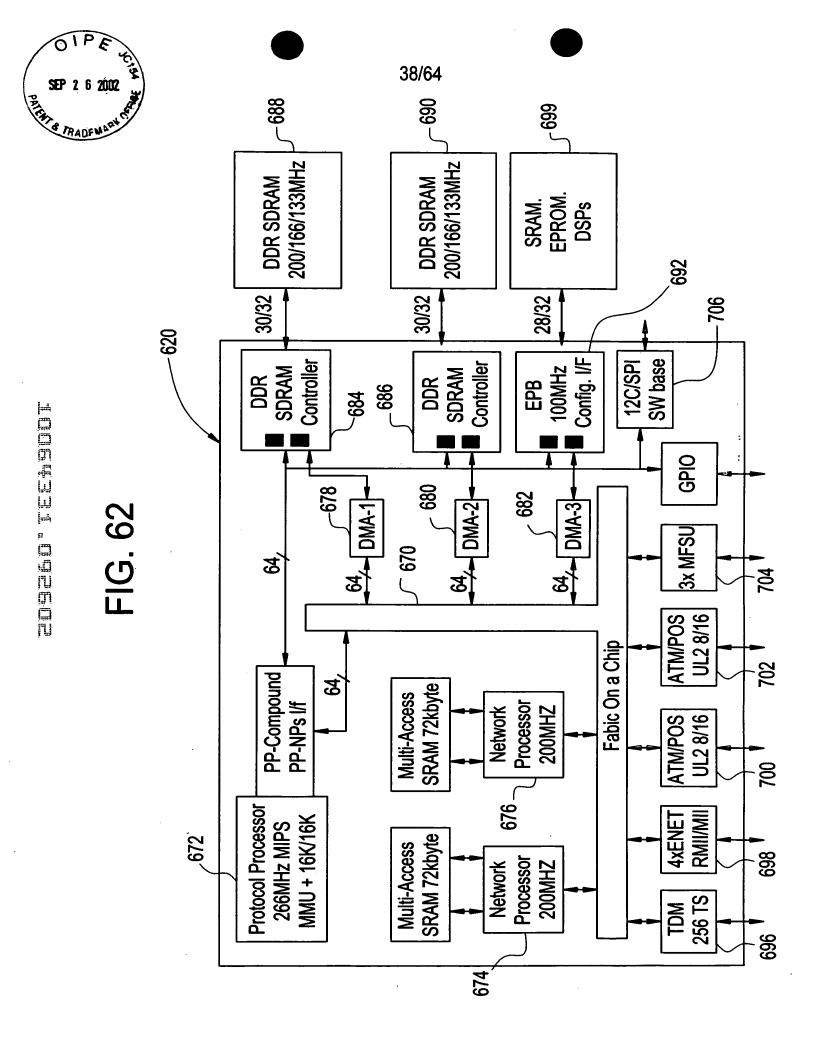




FIG. 63

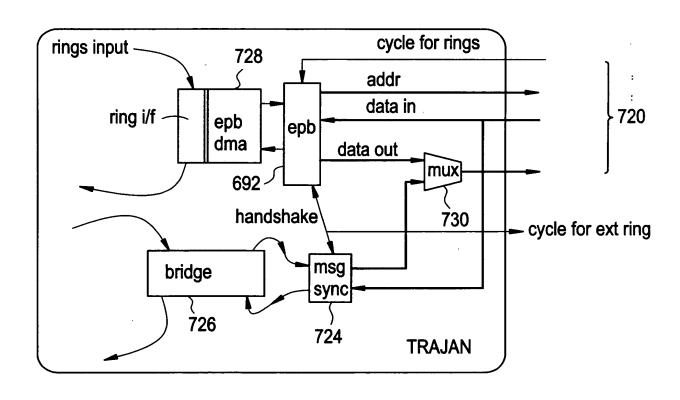
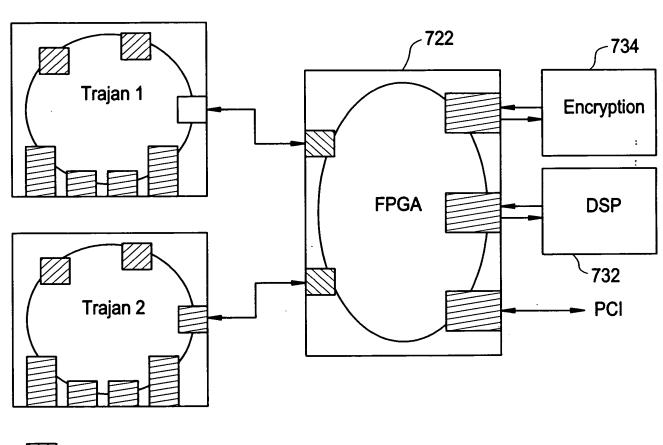




FIG. 64



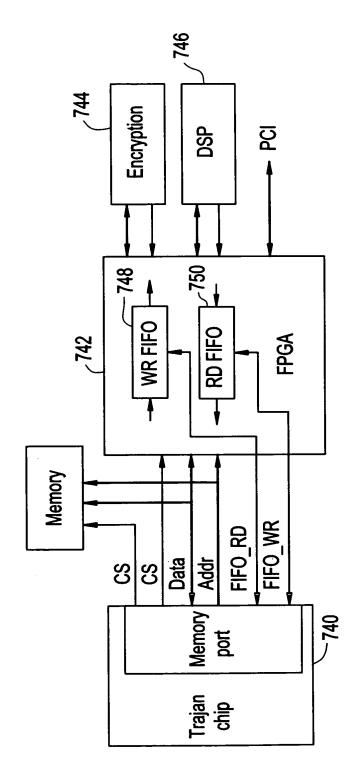
Processor

Functional unit

External ring interface



FIG. 65



:

:



FIFO_WR FIFO_RD ADDR DATA -760 છ Memory controller Trajan 740 Read request RD FIFO used words counter FIG. 66 generator Message sender Host #2 (DMA) Host #1 (PP) _ Host #3 (Ring extender) WR FIFO used words counter Write request generator DATA DATA 옷 웅 interface Ring



FIG. 67

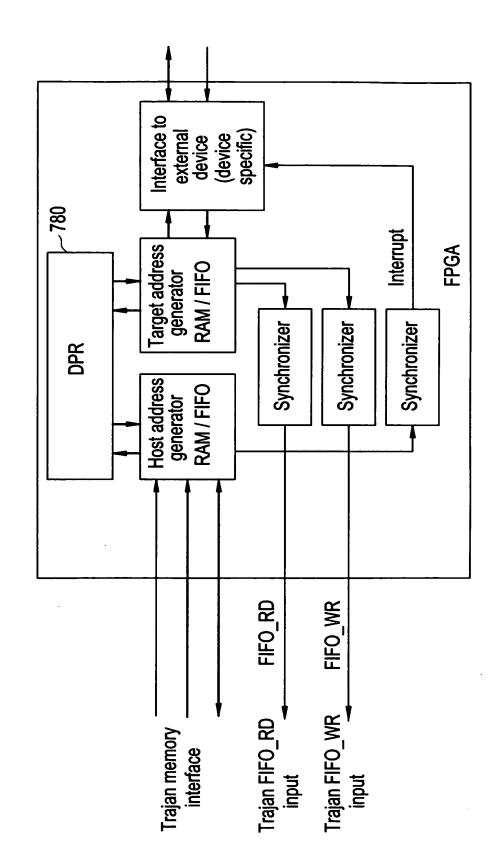




FIG. 68

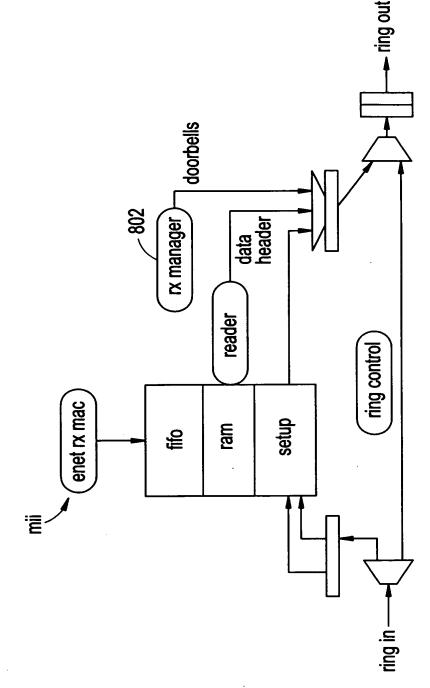




FIG. 69

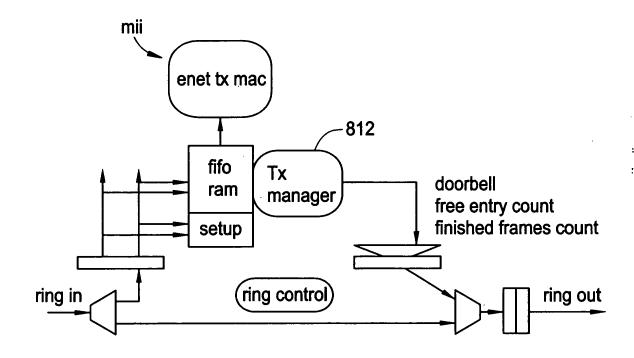
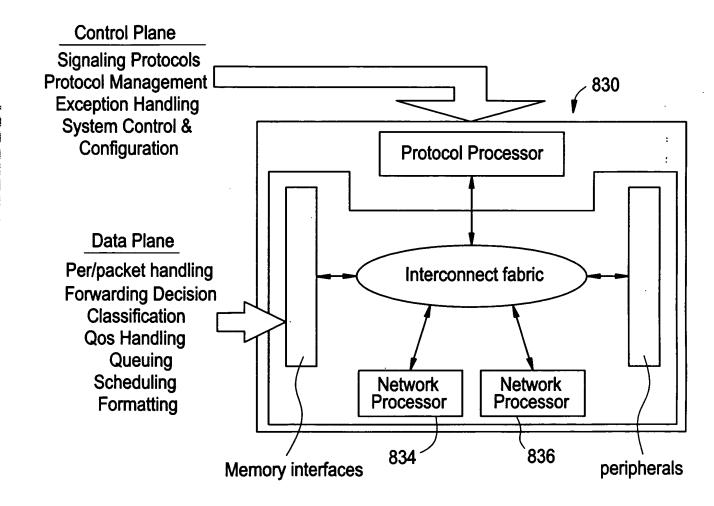
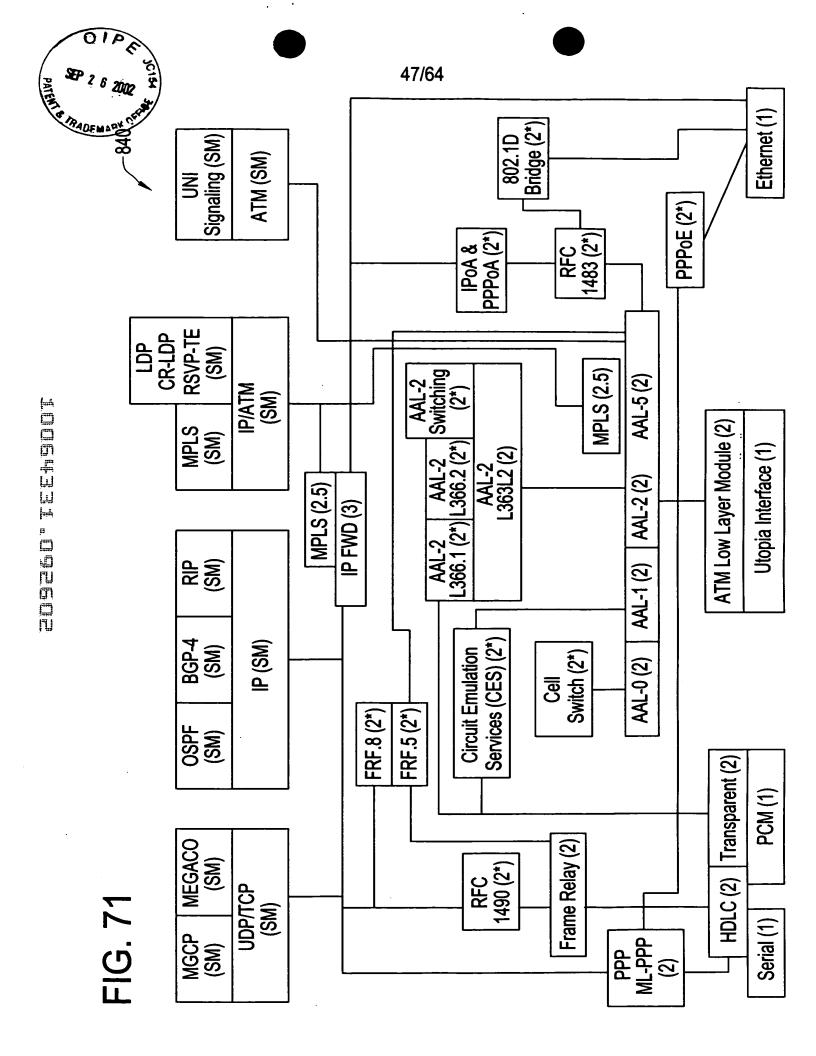




FIG. 70





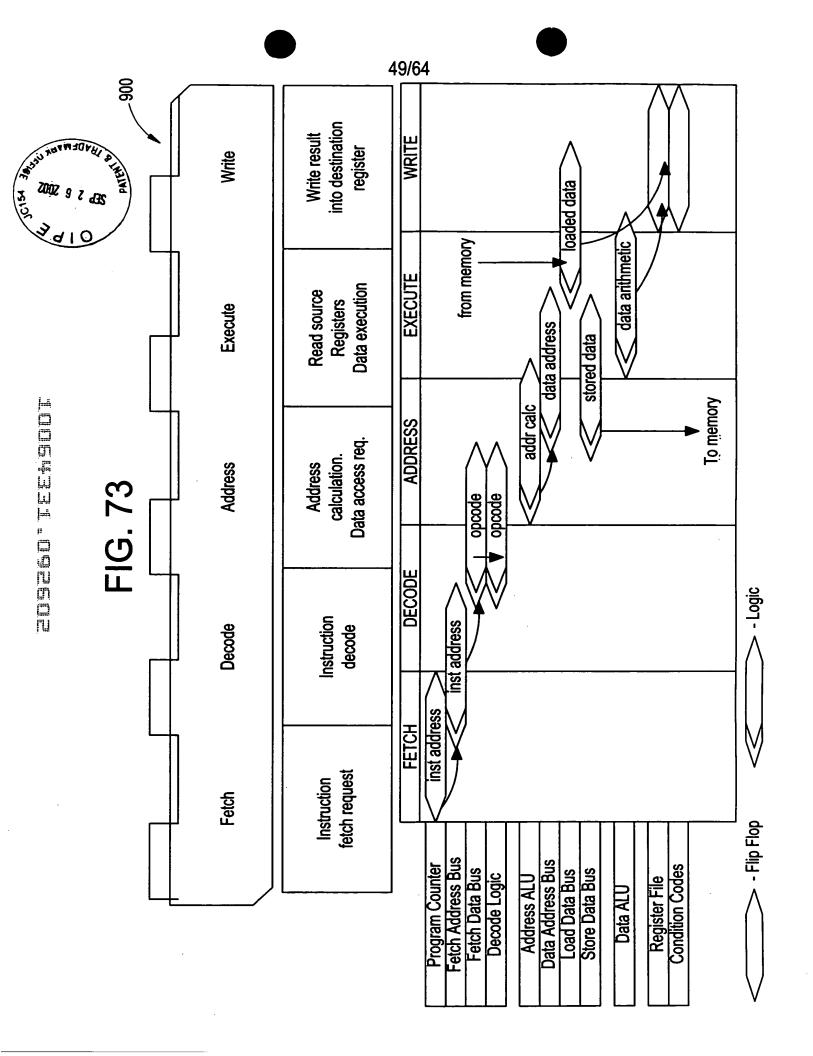




FIG. 74

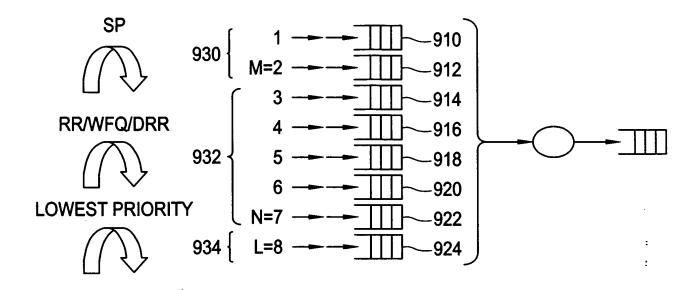
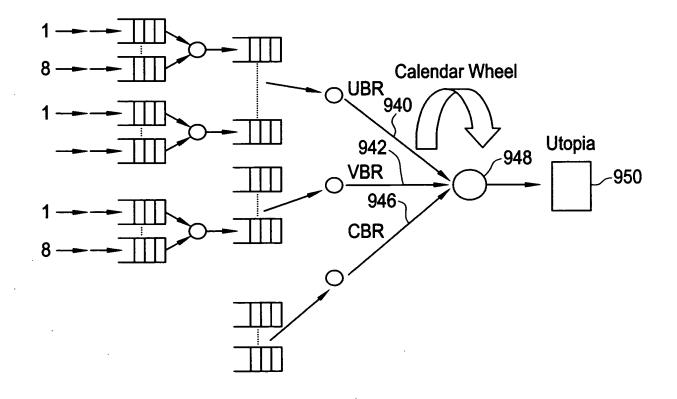
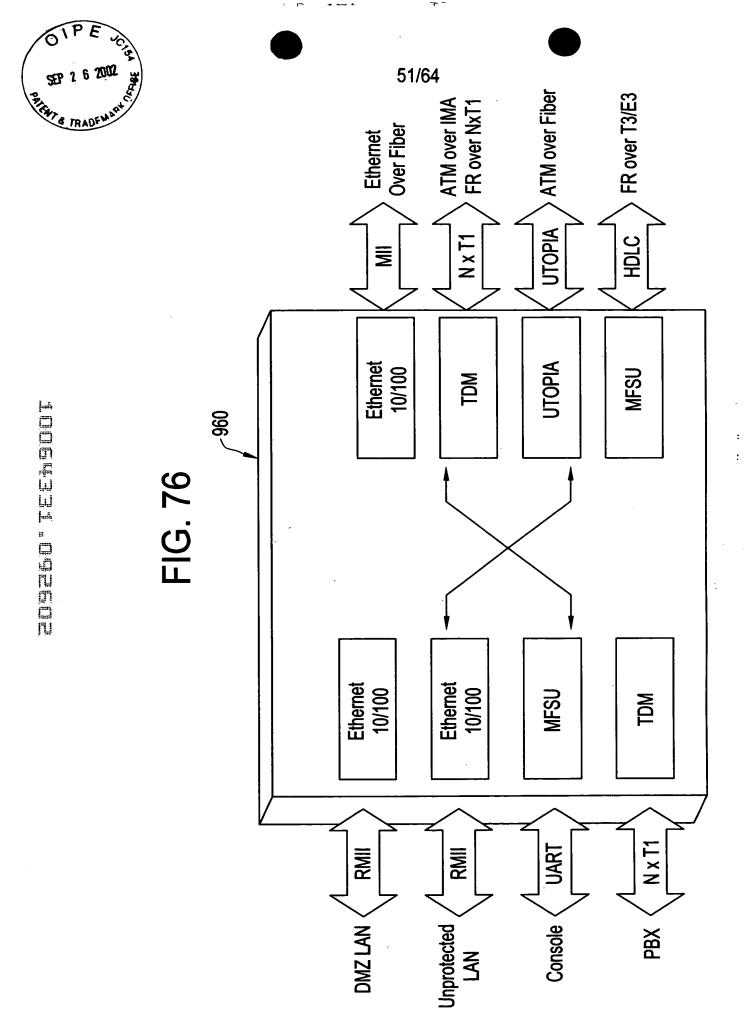
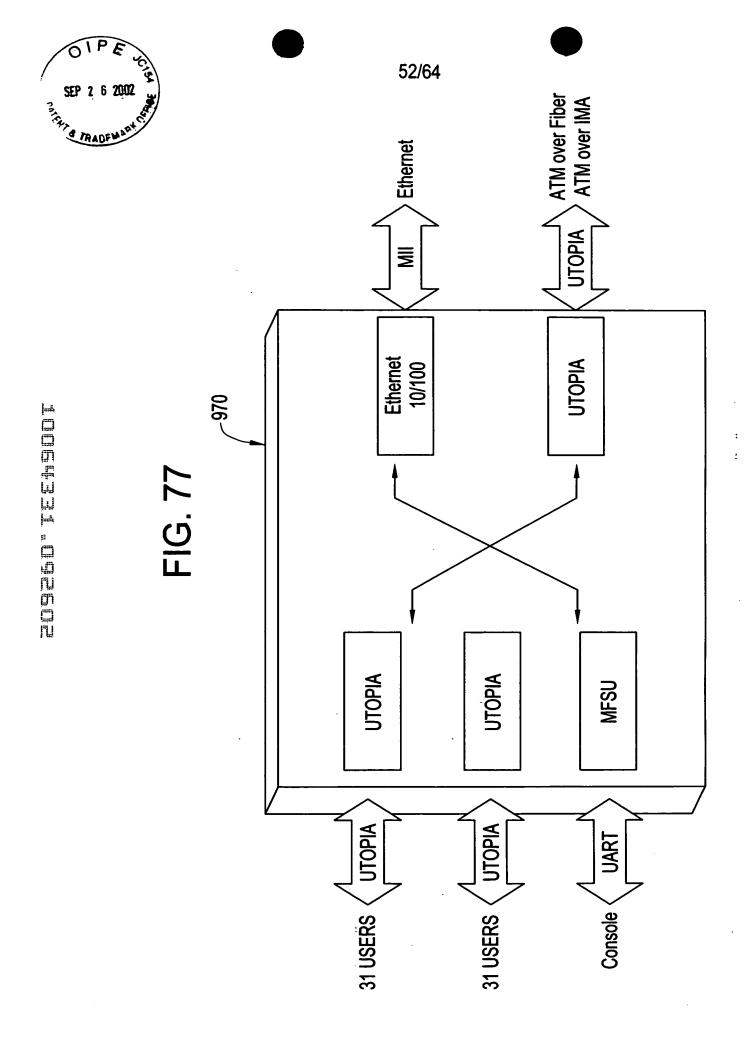
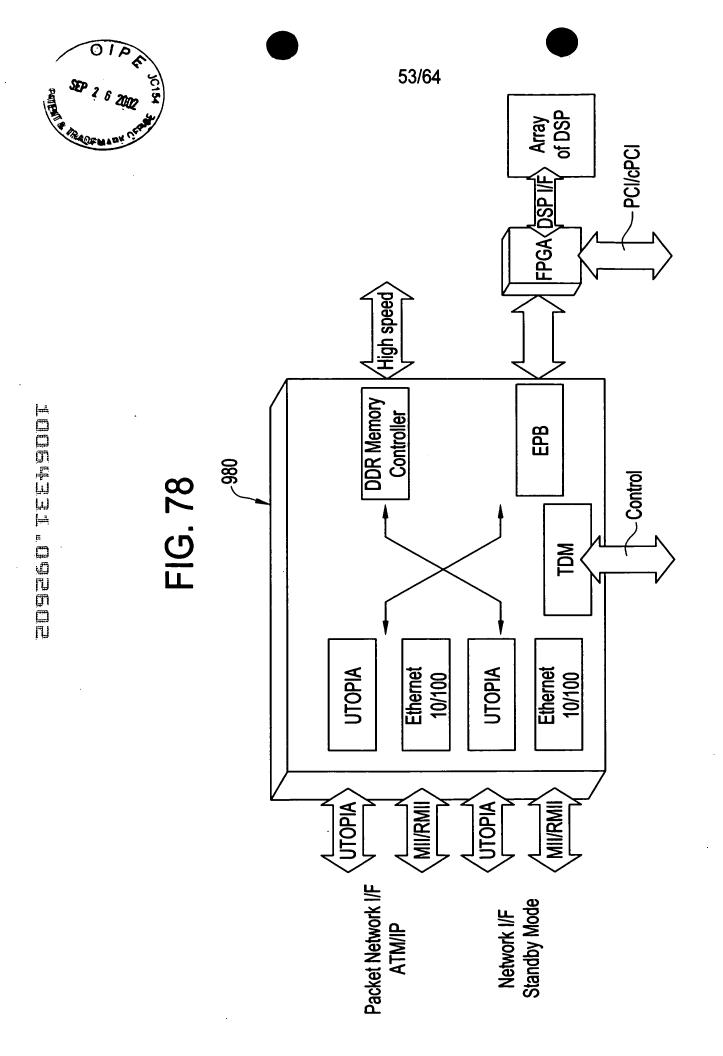


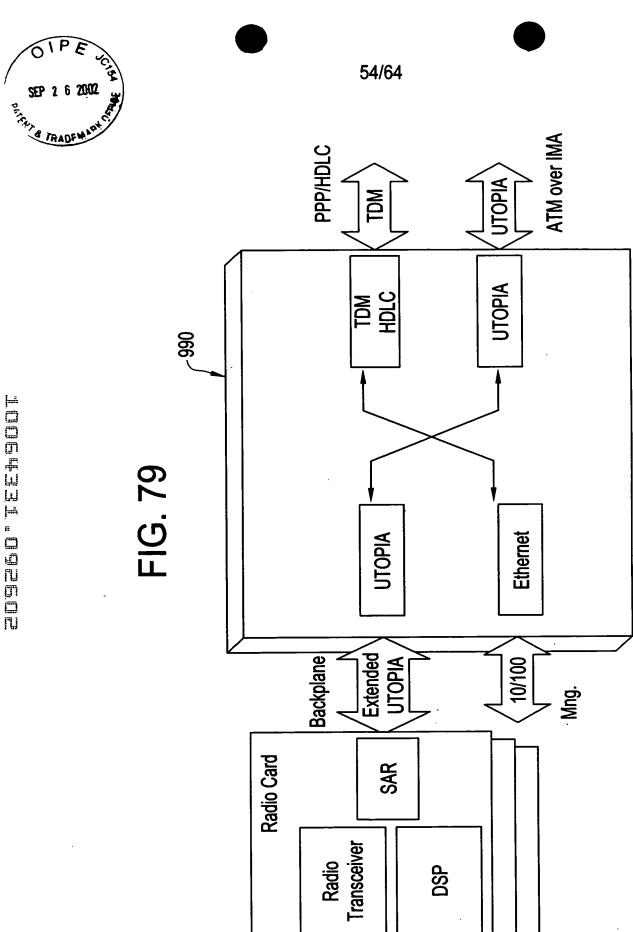
FIG. 75











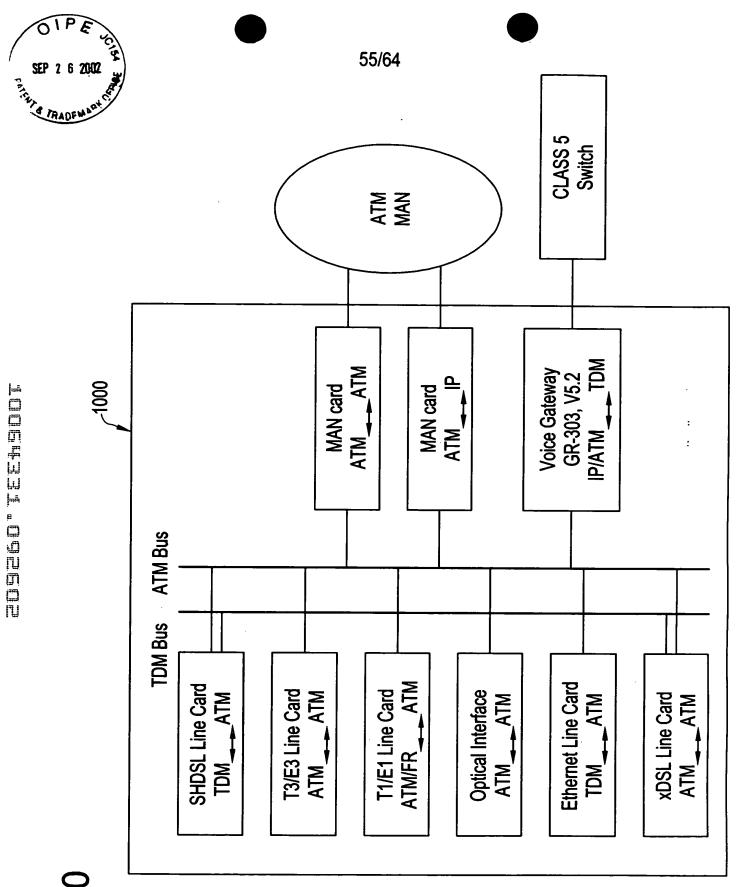
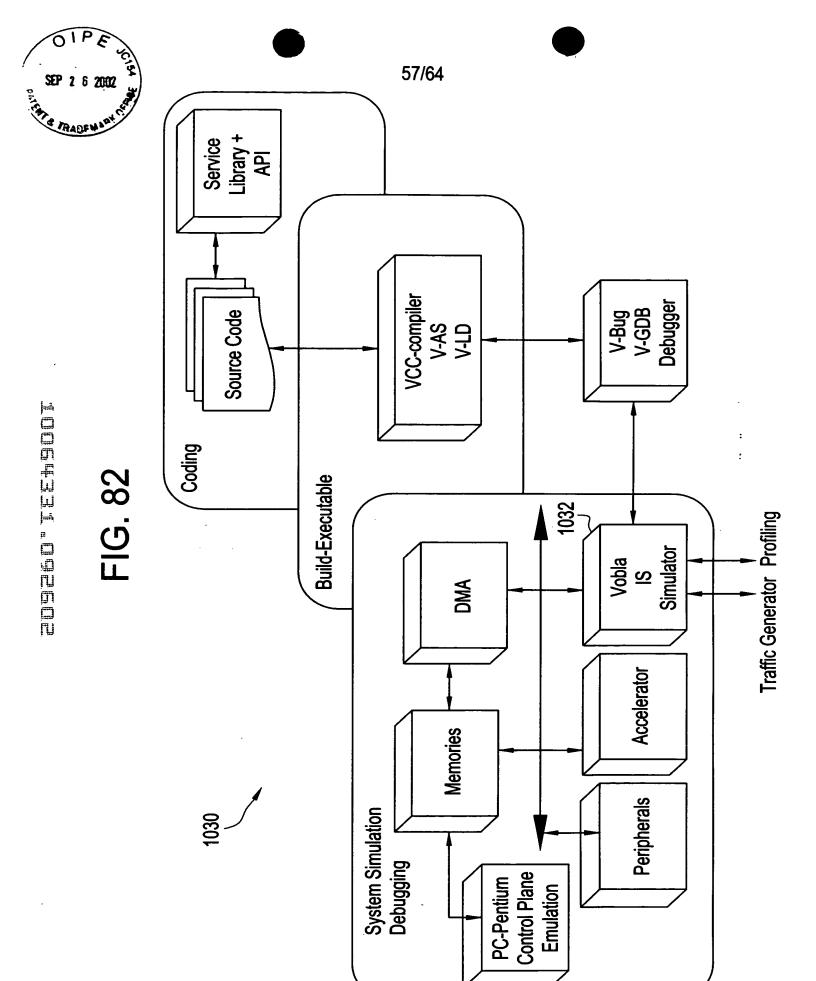


FIG. 80



Shared RAM

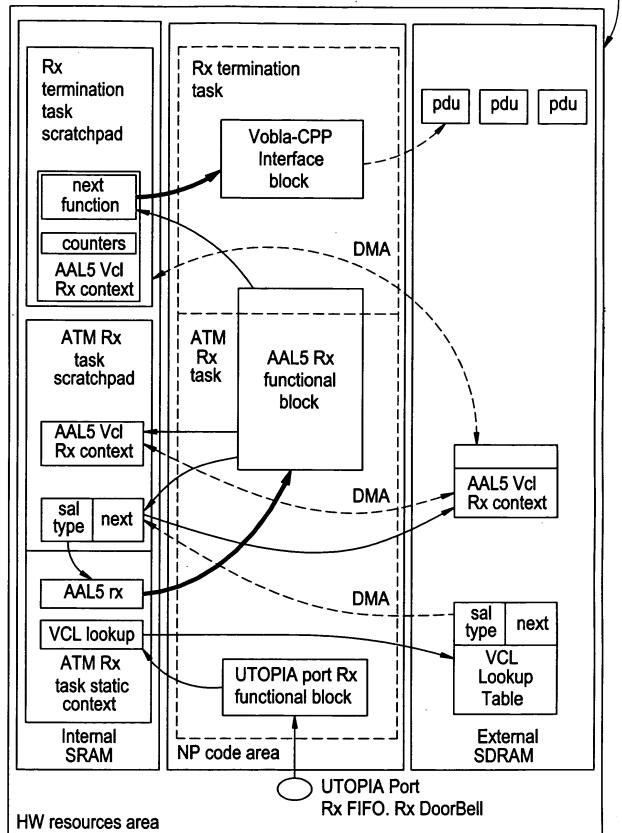
HOOGKUUT GORGOR

TOOKHULL OGESOE

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FIG. 84

1050



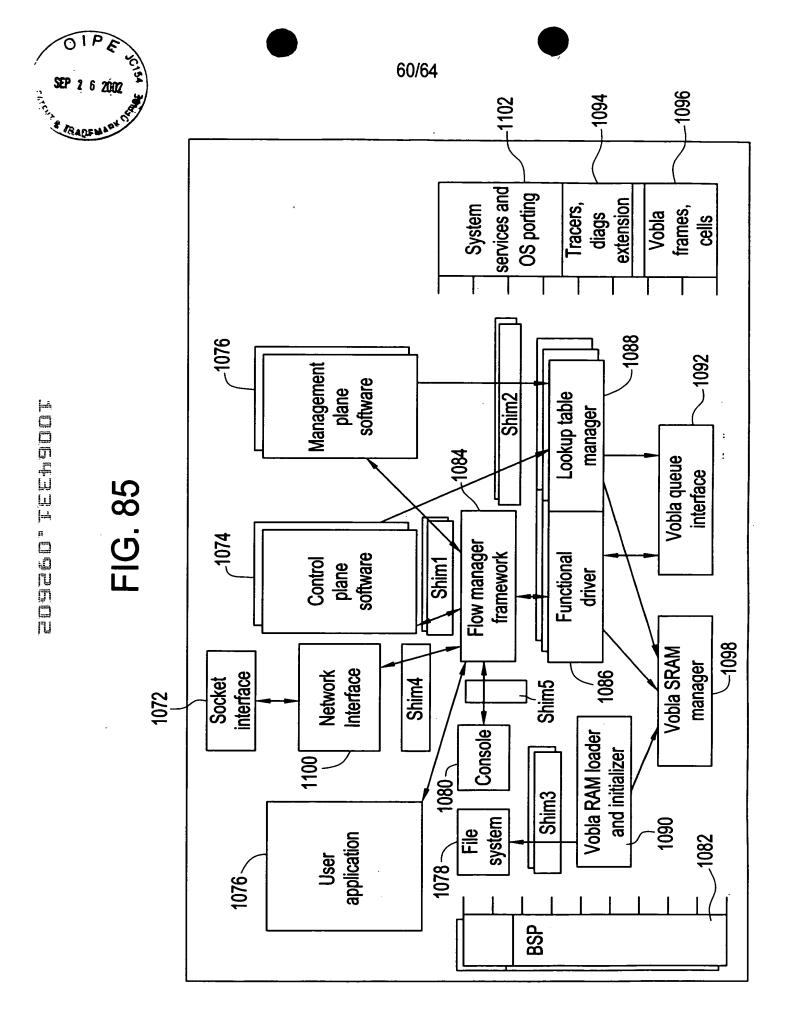


FIG. 86

SEP 2 6 2002

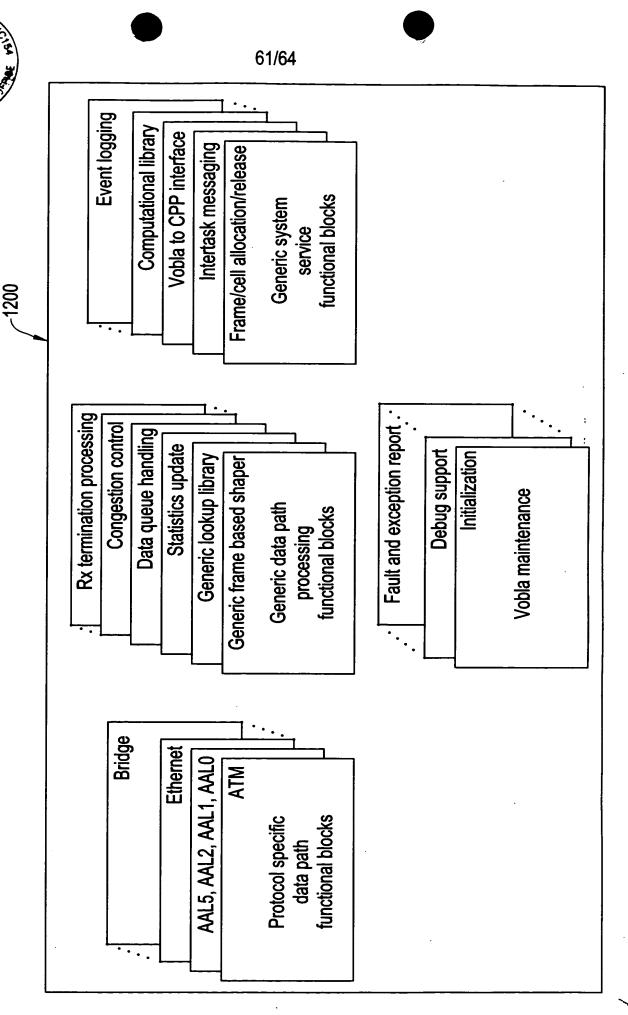




FIG. 87 PRIOR ART

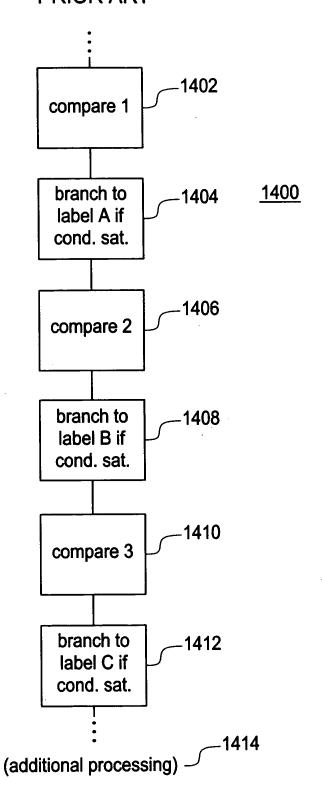




FIG. 88

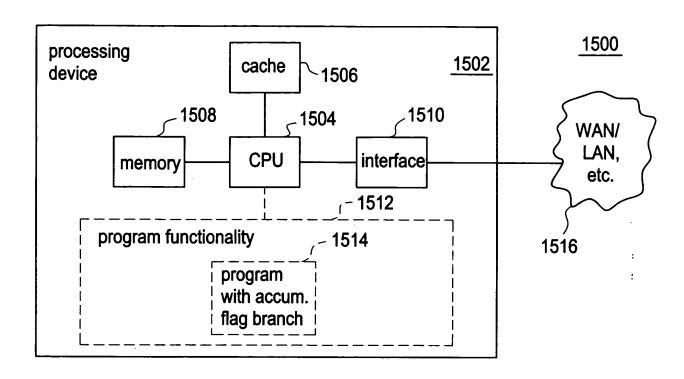


FIG. 89

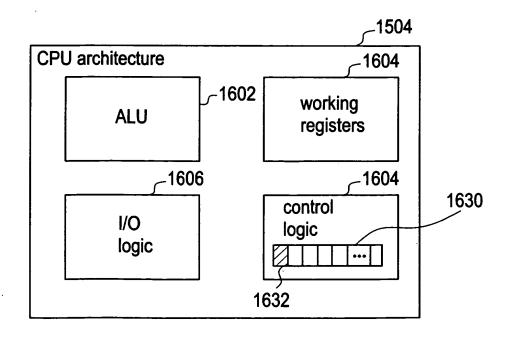


FIG. 90

